# **TVS Diodes**

# **SLD Series**

pertise Applied | Answers Delivered

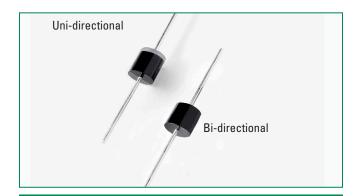
# AUTOMOTIVE GRADE HE ROHS SUM (3)











#### **Agency Approvals**

AGENCY	AGENCY FILE NUMBER
<i>717</i>	E230531

### **Maximum Ratings and Thermal Characteristics** (T<sub>a</sub>=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak Pulse Power Dissipation 10ms x 150ms Test Waveform	P <sub>PPM</sub>	2200	W
Peak Pulse Power Dissipation 10µs x 1000µs Test Waveform	P <sub>PPM</sub>	P <sub>PPM</sub> 5000	
Steady STLte Power Dissipation on Infinite Heat Sink at $T_L=75^{\circ}\text{C}$ (Fig. 6)	P <sub>D</sub>	8.0	W
Peak Forward Surge Current, 8.3ms Single Half Sine Wave (Note 3)	I <sub>FSM</sub>	600	А
Maximum InsTLnTLneous Forward VoITLge at 100A for Unidirectional Only	V <sub>F</sub>	3.5	V
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 175	°C
Typical Thermal ResisTLnce Junction to Lead	R <sub>uJL</sub>	8.0	°C/W
Typical Thermal ResisTLnce Junction to Ambient	R <sub>uJA</sub>	40	°C/W

#### **Description**

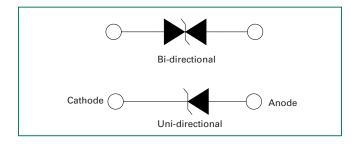
The AEC-Q101 qualified SLD Series is packaged in a highly reliable industry standard P600 axial leaded package and is designed to provide precision overvoltage protection for sensitive electronics.

#### **Features**

- Hi reliability application and automotive grade AEC-Q101 qualified with  $T_J$  • ESD protection of data
- $V_{BR}$  @  $T_{J} = V_{BR}$  @  $25^{\circ}$  C  $\times (1 + \alpha T \times (T_1 - 25))$ (a T:Temperature Coefficient, typical value is 0.1%)
- Glass passivated chip junction in P600 package
- Meet ISO7637 and ISO16750 load dump test; 2200W peak pulse capability at 10µs × 150ms waveform, repetition rate (duty cycles): 0.01%
- Fast response time: typically less than 1.0ps from 0 Volts to BV min
- Excellent clamping capability
- Typical failure mode is short from over-specified voltage or current
- Whisker test is conducted based on JEDEC JESD201A per its table 4a and 4c

- IEC-61000-4-2 ESD 30kV(Air), 30kV (Contact)
- lines in accordance with IEC 61000-4-2
- EFT protection of data lines in accordance with IEC 61000-4-4
- Low incremental surge resistance
- High temperature soldering guaranteed: 260°C+5/-0°C / 10s ±1sec / 0.375",(9.5mm) lead length, 5 lbs., (2.3kg) tension
- Plastic package is flammability rated V-0 per Underwriters Laboratories
- Matte tin lead-free plated
- Halogen free and RoHS compliant
- Pb-free E3 means 2nd level interconnect is Pbfree and the terminal finish material is tin(Sn) (IPC/ JEDEC J-STD-609A.01)

### **Functional Diagram**



#### **Applications**

Designed to protect sensitive electronics from:

- Inductive Load Switching
- Alternator Load Dump



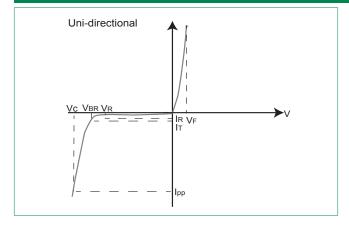
# Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

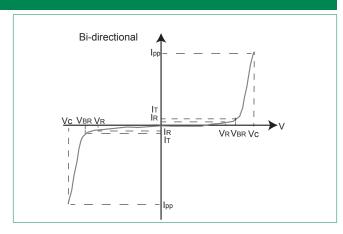
Part Number (Uni)	Part Number (Bi)	Break Voltage (\	V <sub>BR</sub> @ I <sub>T</sub>	Test Current	Reverse Stand off Voltage V <sub>R</sub>	Maximum Reverse Leakage @ V <sub>R</sub>	Maximum Peak Pulse Current	Maximum Clamping Voltage @ I <sub>PP</sub>	Agency Approval
		MIN	MAX	(mA)	(Volts)	I <sub>R</sub> (μΑ)	I <sub>рр</sub> (А)	V <sub>c</sub> (V)	
SLD10U-017	SLD10-018	11.8	13.0	5.0	10	10	300.0	17.0	Х
SLD11U-017	SLD11-018	12.2	13.5	5.0	11	10	280.2	18.2	Х
SLD12U-017	SLD12-018	13.3	14.7	5.0	12	10	256.3	19.9	X
SLD13U-017	SLD13-018	14.4	15.9	5.0	13	10	237.2	21.5	Х
SLD14U-017	SLD14-018	15.6	17.2	5.0	14	10	219.8	23.2	X
SLD15U-017	SLD15-018	16.7	18.5	5.0	15	10	209.0	24.4	Х
SLD16U-017	SLD16-018	18.0	19.3	5.0	16	10	196.2	26.0	X
SLD17U-017	SLD17-018	18.9	20.9	5.0	17	10	184.8	27.6	Х
SLD18U-017	SLD18-018	20.0	22.1	5.0	18	10	174.7	29.2	Х
SLD20U-017	SLD20-018	22.2	24.5	5.0	20	10	157.4	32.4	Х
SLD22U-017	SLD22-018	24.4	26.9	5.0	22	10	143.7	35.5	Х
SLD24U-017	SLD24-018	26.7	29.5	5.0	24	10	131.1	38.9	Х
SLD26U-017	SLD26-018	28.9	31.9	5.0	26	10	121.1	42.1	Х
SLD28U-017	SLD28-018	31.1	34.4	5.0	28	10	112.3	45.4	Х
SLD30U-017	SLD30-018	33.3	36.8	5.0	30	10	105.4	48.4	Х
SLD33U-017	SLD33-018	36.7	40.6	5.0	33	10	95.7	53.3	Х
SLD36U-017	SLD36-018	40.0	44.2	5.0	36	10	87.8	58.1	Х
SLD40U-017	SLD40-018	44.4	49.1	5.0	40	10	79.1	64.5	Х
SLD43U-017	SLD43-018	49.0	54.2	5.0	43	10	73.5	69.4	Х
SLD45U-017	SLD45-018	50.0	55.3	5.0	45	10	70.2	72.7	Х
SLD48U-017	SLD48-018	53.3	58.9	5.0	48	10	65.9	77.4	Х
SLD51U-017	SLD51-018	56.7	62.7	5.0	51	10	61.9	82.4	Х
SLD54U-017	SLD54-018	60.0	66.3	5.0	54	10	58.6	87.1	Х
SLD58U-017	SLD58-018	64.4	71.2	5.0	58	10	54.5	93.6	Х
SLD60U-017	SLD60-018	68.4	75.6	5.0	60	10	52.7	96.8	X

#### Notes:

- 1. VBR measured after IT applied for 300µs, IT= square wave pulse or equivalent.
- 2. Surge current waveform per 10µs x 1000µs exponential wave and derated per Fig. 4.
- 3. All terms and symbols are consistent with ANSI/IEEE C62.35

#### **I-V Curve Characteristics**





- P<sub>PPM</sub> Peak Pulse Power Dissipation Max power dissipation
- V<sub>s</sub> Stand-off Voltage -- Maximum voltage that can be applied to the TVS without operation
- V<sub>ss</sub> Breakdown Voltage Maximum voltage that flows though the TVS at a specified test current (I,)
- V<sub>c</sub> Clamping Voltage Peak voltage measured across the TVS at a specified Ippm (peak impulse current)
- Reverse Leakage Current -- Current measured at V.
- Forward Voltage Drop for Uni-directional V,



Ratings and Characteristic Curves (T<sub>A</sub>=25°C unless otherwise noted)

Figure 1 - TVS Transients Clamping Waveform

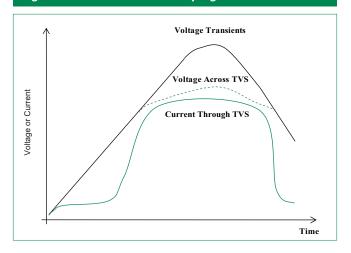


Figure 2 - Peak Pulse Power Rating Curve

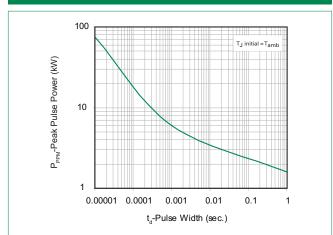


Figure 3 - Peak Pulse Power Derating Curve

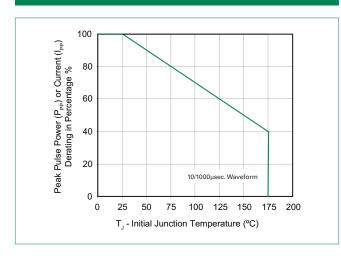


Figure 4 - Pulse Waveform

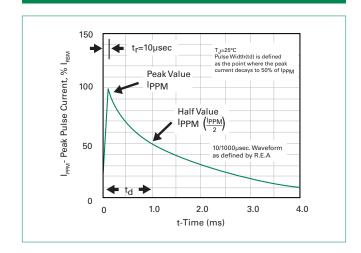


Figure 5 - Typical Junction Capacitance

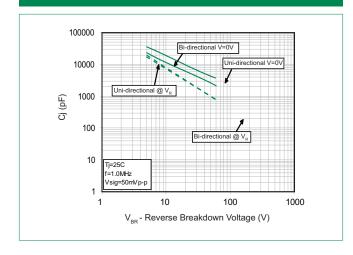
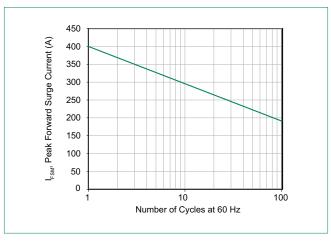
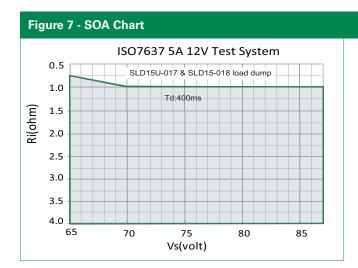
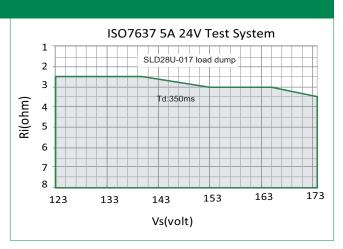


Figure 6 - Maximum Non-Repetitive Peak Forward Surge Current









## **Soldering Parameters**

## Flow/Wave Soldering (Solder Dipping)

Peak Temperature :	260°C+5/-0°C	
Dipping Time :	10s ±1 seconds	
Soldering :	1 time	

## **Physical Specifications**

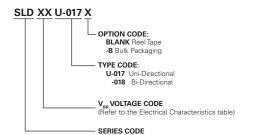
Weight	0.07oz., 2.1g
Case	P600 molded plastic body over passivated junction.
Polarity	Color band denotes the cathode except Bipolar.
Terminal	Matte Tin axial leads, solderable per JESD22-B102.

## **Environmental Specifications**

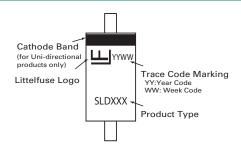
High Temp. Storage	JESD22-A103	
HTRB	JESD22-A108	
Temperature Cycling	JESD22-A104	
H3TRB	JESD22-A101	
RSH	JESD22-B106	



## **Part Numbering System**



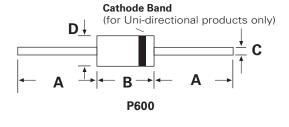
## **Part Marking System**



### **Packing Options**

Part Number	Component Package	Quantity	Packaging Option	Packaging Specification
SLDxxXXX	P600	800	Tape & Reel	EIA STD RS-296
SLDxxXX-B	P600	100	вох	Littelfuse Spec.

### **Dimensions**



Dimensions	Incl	hes	Millimeters		
	Min	Max	Min	Max	
А	1.000	-	25.40	-	
В	0.340	0.360	8.60	9.10	
С	0.048	0.052	1.22	1.32	
D	0.340	0.360	8.60	9.10	

## **Tape and Reel Specification**

