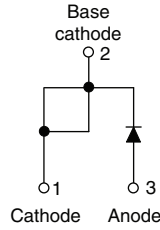




High Performance Schottky Rectifier, 15 A



TO-220AC



FEATURES

- 150 °C T_J operation
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



PRODUCT SUMMARY	
Package	TO-220AC
I _{F(AV)}	15 A
V _R	60 V
V _F at I _F	0.56 V
I _{RM} max.	45 mA at 125 °C
T _J max.	150 °C
Diode variation	Single die
E _{AS}	6 mJ

DESCRIPTION

The VS-15TQ060... Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
I _{F(AV)}	Rectangular waveform	15	A
V _{RRM}		60	V
I _{FSM}	t _p = 5 µs sine	1000	A
V _F	15 A _{pk} , T _J = 125 °C	0.56	V
T _J	Range	-55 to 150	°C

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-15TQ060PbF	VS-15TQ060-N3	UNITS
Maximum DC reverse voltage	V _R	60	60	V
Maximum working peak reverse voltage	V _{RWM}			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 104 °C, rectangular waveform		15	A
Maximum peak one cycle non-repetitive surge current See fig. 7	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1000	
		10 ms sine or 6 ms rect. pulse		260	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.50 A, L = 11.5 mH		6	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.50	A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	$V_{FM}^{(1)}$	15 A	$T_J = 25\text{ }^\circ\text{C}$	0.62	V
		30 A		0.82	
		15 A	$T_J = 125\text{ }^\circ\text{C}$	0.56	
		30 A		0.71	
Maximum reverse leakage current See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^\circ\text{C}$	$V_R = \text{Rated } V_R$	0.80	mA
		$T_J = 125\text{ }^\circ\text{C}$		45	
Maximum junction capacitance	C_T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz) $25\text{ }^\circ\text{C}$		720	pF
Typical series inductance	L_S	Measured lead to lead 5 mm from package body		8	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	V/ μ s

Note(1) Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}			-55 to 150	$^\circ\text{C}$
Maximum thermal resistance, junction to case	R_{thJC}	DC operation See fig. 4		3.25	$^\circ\text{C/W}$
Typical thermal resistance, case to heatsink	R_{thCS}	Mounting surface, smooth and greased		0.50	
Approximate weight				2	g
				0.07	oz.
Mounting torque	minimum			6 (5)	kgf · cm (lbf · in)
	maximum			12 (10)	
Marking device		Case style TO-220AC (JEDEC)		15TQ060	

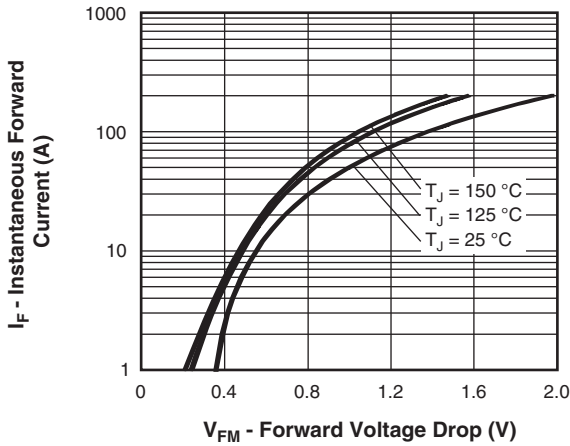


Fig. 1 - Maximum Forward Voltage Drop Characteristics

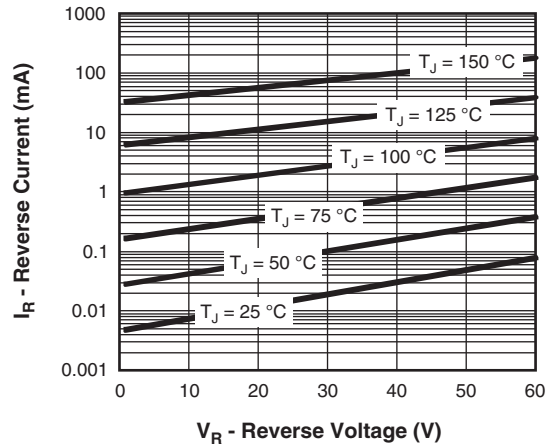


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

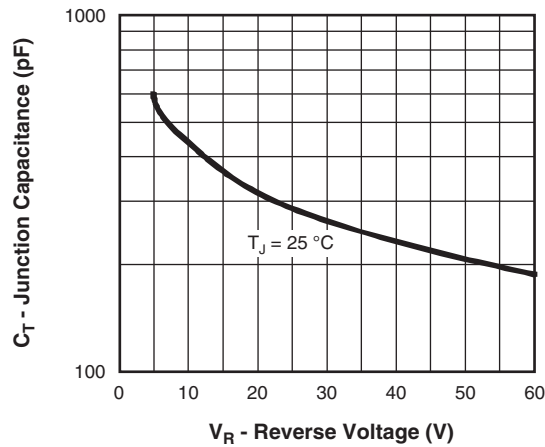


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

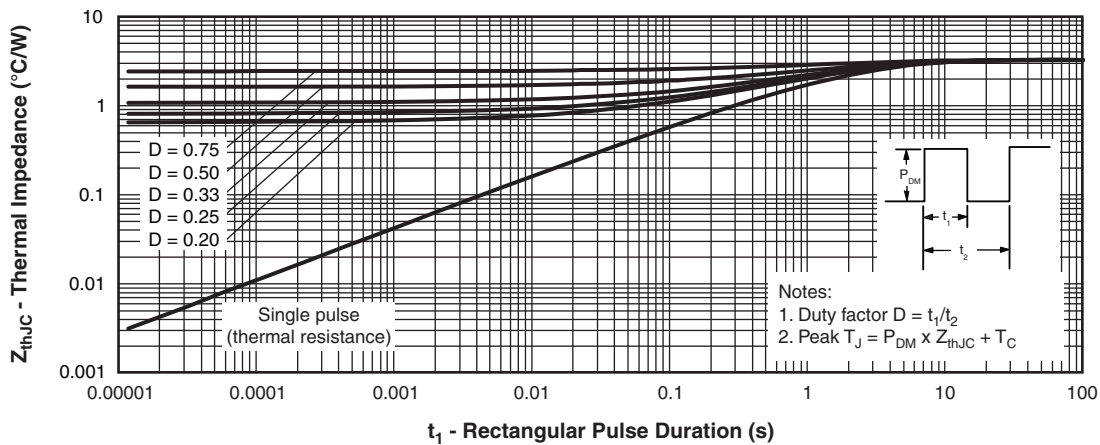


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

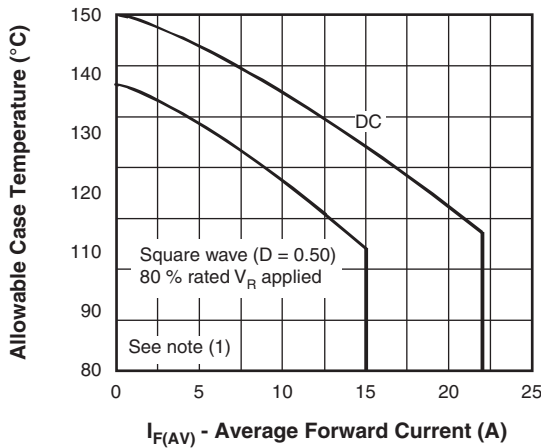


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

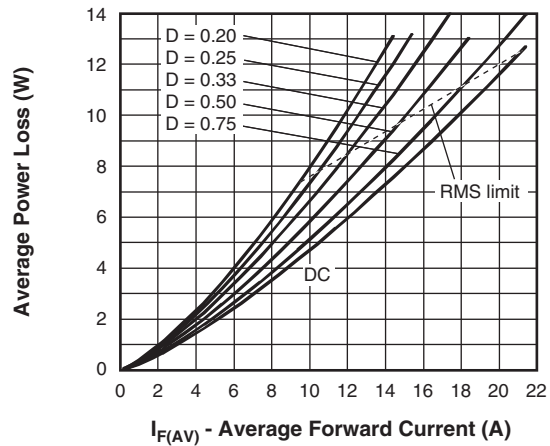


Fig. 6 - Forward Power Loss Characteristics

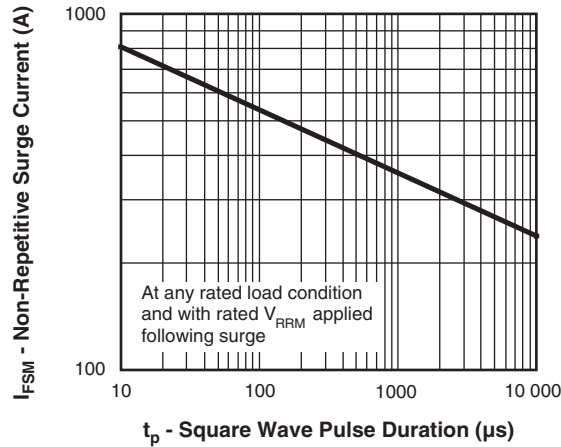


Fig. 7 - Maximum Non-Repetitive Surge Current

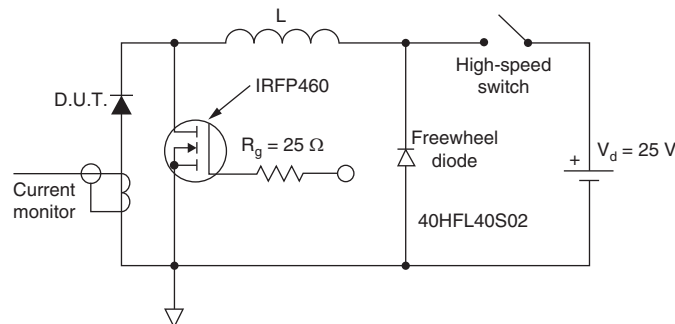


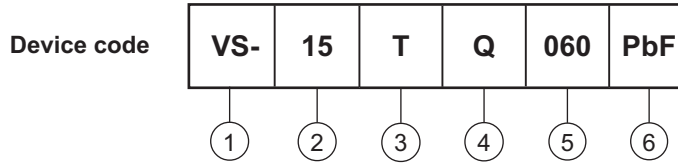
Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 $P_{d_{REV}}$ = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R



ORDERING INFORMATION TABLE



- 1** - Vishay Semiconductors product
- 2** - Current rating (15 = 15 A)
- 3** - Package:
T = TO-220
- 4** - Schottky "Q" series
- 5** - Voltage rating (060 = 60 V)
- 6** - Environmental digit
 - PbF = Lead (Pb)-free and RoHS compliant
 - -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)			
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-15TQ060PbF	50	1000	Antistatic plastic tube
VS-15TQ060-N3	50	1000	Antistatic plastic tube

LINKS TO RELATED DOCUMENTS		
Dimensions		www.vishay.com/doc?95221
Part marking information	TO-220ACPbF	www.vishay.com/doc?95224
	TO-220AC-N3	www.vishay.com/doc?95068
SPIICE model		www.vishay.com/doc?95600



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